



Attorney Docket no: SEL 246

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Koichiro TANAKA )  
Serial No.: 09/812,529 )  
Filed: March 20, 2001 )  
For: Method of Manufacturing A Semiconductor )  
Device )  
Examiner: R. Booth )  
Art Unit: 2812 )

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:  
Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450 on  
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Shannon Wallace  
Name of applicant, assignee, or Registered Rep.  
Shannon Wallace 10/20/03  
Signature Date

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 23313-1450

AMENDMENT E

In furtherance of Response D (After Final) in response to the Final Rejection of May 22, 2003, and in response to the Advisory Action of September 24, 2003 a two month extension of time and RCE being submitted herewith, please amend the above-identified application as follows: